



Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

1. (Currently amended) An apparatus comprising:  
a charge pump having a capacity that is preset to a particular value to simultaneously affect a first number of flash memory cells at a particular voltage; and  
a measuring circuit including a voltage sensor to sense a voltage at an input and output of the charge pump and a temperature sensor to measure the temperature of the charge pump, wherein the measuring circuit is configured to measure an actual capacity of the charge pump and to reset the capacity of the charge pump to a second value based on the measured capacity to simultaneously affect a second number of flash memory cells.
2. (Original) The apparatus of claim 1 wherein an output of the charge pump is preset to operate at particular voltage and current levels.
3. - 5. (Canceled)
6. (Original) The apparatus of claim 1 wherein the measuring circuit includes a current sensor to sense a current at an output of the charge pump.
7. (Currently amended) An apparatus comprising:  
an array of memory cells; and  
a charge pump circuit coupled to the array of memory cells to drive the array of memory cells, the charge pump circuit comprising:  
a charge pump having a capacity that is preset to a particular value to simultaneously affect a first number of flash memory cells at a particular voltage; and

a measuring circuit including a voltage sensor to sense a voltage at an input and output of the charge pump and a temperature sensor to measure the temperature of the charge pump, wherein the measuring circuit is configured to measure an actual capacity of the charge pump and to reset the capacity of the charge pump to a second value based on the measured capacity to simultaneously affect a second number of flash memory cells.

8. (Original) The apparatus of claim 7 wherein an output of the charge pump is preset to operate at particular voltage and current levels.

9. - 11. (Canceled)

12. (Original) The apparatus of claim 7 wherein the measuring circuit includes a current sensor to sense a current at an output of the charge pump.

13. (Currently amended) A computer system comprising:  
a central processor; and  
a memory coupled to the central processor, the memory comprising:  
an array of memory cells, and  
a charge pump circuit coupled to the array of memory cells to drive the array of memory cells, the charge pump circuit comprising:  
a charge pump having a capacity that is preset to a particular value to simultaneously affect a first number of flash memory cells at a particular voltage, and

a measuring circuit including a voltage sensor to sense a voltage at an input and output of the charge pump and a temperature sensor to measure the temperature of the charge pump, wherein the measuring circuit is configured to measure an actual capacity of the charge pump and to reset the capacity of the charge pump to a second value based on the measured capacity to simultaneously affect a second number of flash memory cells.

14. (Original) The computer system of claim 13 wherein an output of the charge pump is preset to operate at particular voltage and current levels.

15. – 17. (Canceled)

18. (Original) The computer system of claim 13 wherein the measuring circuit includes a current sensor to sense the current at an input of the charge pump.

19. – 25. (Canceled)

26. (Previously Presented) A method comprising:  
establishing a nominal value for a capacity of a charge pump sufficient to simultaneously affect a first number of flash memory cells at a particular voltage;  
characterizing an actual capacity of the charge pump by measuring an output voltage level of the charge pump while an output current of the charge pump is kept at a constant value;  
and  
resetting the nominal capacity value of the charge pump to a second capacity value based on the actual capacity,  
wherein the second capacity value of the charge pump is sufficient to simultaneously affect a second number of flash memory cells.

27. (Previously Presented) The method of claim 25, wherein the second capacity value is reset by resetting the output voltage level of the charge pump.

28. (Previously Presented) The method of claim 25, wherein the second number of flash memory cells is greater than the first number of memory cells.

29. (Previously Presented) The method of claim 26, wherein characterizing the charge pump includes a temperature sensor to measure the temperature of the charge pump.

30. (Previously Presented) A method comprising:  
establishing a nominal value for a capacity of a charge pump sufficient to simultaneously affect a first number of flash memory cells at a particular current;  
characterizing an actual capacity of the charge pump by measuring an output current level of the charge pump while an output voltage of the charge pump is kept at a constant value;  
and  
resetting the nominal capacity value of the charge pump to a second capacity value based on the actual capacity,  
wherein the second capacity value of the charge pump is sufficient to simultaneously affect a second number of flash memory cells.

31. (Previously Presented) The method of claim 30, wherein the second capacity value is reset by resetting the output current level of the charge pump.

32. (Previously Presented) The method of claim 30, wherein the second number of flash memory cells is greater than the first number of memory cells.

33. (Previously Presented) The method of claim 30, wherein characterizing the charge pump includes a temperature sensor to measure the temperature of the charge pump

34. (Previously Presented) The method of claim 30, wherein affecting the flash memory cells comprises at least one of programming, erasing and reading the flash memory cells.

35. (Previously Presented) A method comprising:  
recognizing a difference between a nominal capacity value of a charge pump sufficient to simultaneously affect a first number of flash memory cells and an actual capacity value sufficient to simultaneously affect a second number of flash memory cells;  
resetting the nominal value to the actual capacity value; and

using the actual capacity value of the charge pump to simultaneously affect the second number of flash memory cells,

wherein the second number of flash memory cells is greater than the first number of flash memory cells.